

BACHELOR OF TECHNOLOGY
ELECTRONICS ENGINEERING (VLSI DESIGN AND TECHNOLOGY)
THIRD YEAR (SIXTH SEMESTER)
W.E.F. ADMISSION BATCH 2023-24

Sl. No.	Category	Course Code	Course	Contact Hrs. L-T-P	Credit	University Marks	Internal Evaluation
Subject (Theory)							
1	PC	VDPC3004	VLSI Testing	3-0-0	3	100	50
2	PC	VDPC3005	VLSI Fabrication Technology	3-0-0	3	100	50
3	PE	VDPE3005	VLSI Algorithm	3-0-0	3	100	50
		CSPE3010	Embedded Systems				
		EOPE3005	Optical and Satellite Communication				
		EOPE3008	Antenna Analysis and Synthesis				
		-	-				
		-	-				
4	PE	VDPE3009	Design Verification Using System Verilog	3-0-0	3	100	50
		VDPE3010	MEMS				
		VDPE3011	Mixed Signal IC Design				
		VDPE3012	Embedded SoC and Cyber Physical System				
		-	-				
		-	-				
5	HS	HSHS3003	Professional Ethics	3-0-0	2	100	50
		HSHS3002	Entrepreneurship Development				
6	MC	MCMC3002	Industrial Safety Engineering	3-0-0	2	100	50
		MCMC3001	Environmental Engineering				
Subject (Sessional / Practical)							
7	PSI	CSPS3202	Project for Product Development - I	0-0-6	3	-	100
8	PC	VDPC3204	Digital VLSI Laboratory	0-0-3	1.5	-	100
9	PC	VDPC3205	VLSI Design Verification Laboratory	0-0-3	1.5	-	100
Total				18-0-12	22	600	600

NB : Minimum 4 weeks of Summer Internship and Research Experience-II (SIRE-II) after 6th Semester during the vacation.

[Click here to view/download the syllabus of the subjects.](#)